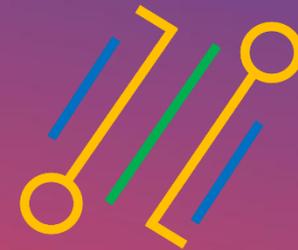


Xecs Matchmaking Event

logiicdev (SME)

Concept to System design



logiicdev

MSc Deepak v Katkoria

logiicdev

contact@logiicdev.eu

Company at a glance

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- Graz, Austria
- 6 FT (including 2 R&I)
- 2020 146K€, 2021 106K€, 2022 76K€, '23 ~80K€
- 2020 10%, 2021 20%, 2022 80%, 2023 >90%
- Product & Market:
 - 1.FPGA based verification/prototype/evaluation & Zync SOM (250\$).
 - 2.Patented High speed Analog design for Digital twin of Energy market.
 - 3.SI&PI Simulation –Macro-Die, Package, Chip & PCB.
 - 4.AI based test equipment for Power supply.
- www.logiicdev.eu / contact@logiicdev.eu

Company's Activities

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- Collaborative projects:

In both personal and team capacities, we've successfully overseen and executed numerous collaborative projects within the realms of mobile, NFC, and PC design. Our dynamic team excels in software (SW), hardware (HW), firmware (FW), as well as Python-based algorithm development, showcasing a robust and comprehensive skill set.

- SRA/Xecs interaction:

- Visiting events
- Participate on roadmaps (Package design)

Collaboration Expectations

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Collaborative project of interest to you	<i>EUREKA Clusters, ECSEL, H2020, Eurostars, or other</i>
Scope	<i>Technology development, Prototype, simulation and design</i>
Project consortium	<p><i>we are searching for leaders to offering expertise in developing, implementing and management from chip to system level design. Specialise in Analog, Digital and programmable logic design and testing. Our curiosity extends to diverse domains.</i></p> <p><i>We eagerly anticipate the opportunity to engage in in-depth discussions with you on these matters.</i></p>